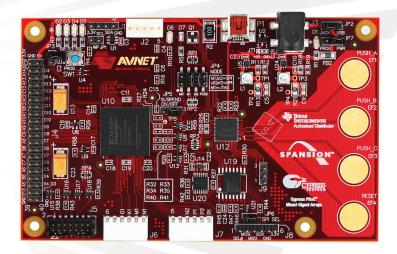
Avnet Product Brief



The Xilinx Spartan-3A Evaluation Kit provides an easy to use, low cost platform for experimenting and prototyping applications based on the Xilinx Spartan-3A FPGA family. Designed as an entry level kit, first time FPGA designers will find the board's functions straight forward and practical, while advanced users will appreciate some of the unique capabilities included in the board design.

The Spartan-3A board uses the 400,000 gate 3S400A FPGA from Xilinx. With over 8,000 logic cells available, this FPGA is ideal for exploring general logic designs as well as DSP and MicroBlaze based embedded processing applications. Besides the FPGA, the board also includes a Cypress PSoC Mixed Signal Array which functions as a "companion chip" to the Spartan-3A. The PSoC device is pre-configured to enable direct programming of the FPGA, SPI Flash memory, and parallel Flash memory through a Windows-based programming application. Spansion SPI and parallel Flash memories are included for FPGA configuration storage, code storage and data file storage. The board's cost optimized power supply from Texas Instruments allows for powering from either a USB port or a 5 V AC/DC wall adapter. Other general purpose peripherals like switches, LEDs, user I/O, and clocks round out the board functions.

Xilinx Spartan-3A Evaluation Kit Includes:

- Evaluation board
- ISE™ WebPACK™ 10.1 DVD
- USB cable
- Windows® programming application
- Cypress[®] MiniProg Programming Unit
- Downloadable documentation and reference designs

Xilinx[®] Spartan[™]-3A Evaluation Kit

Features:

FPGA

- Xilinx XC3S400A-4FTG256C Spartan-3A FPGA
- I/O Peripherals
- Four LEDs
- Four CapSense switches
- I²C temperature sensor

Board I/O Connectors

- Two 6-pin expansion headers
- 20 x 2, 0.1-inch user I/O header

Memory

- 32 Mb Spansion[®] MirrorBit[®] NOR GL Parallel Flash
- 128 Mb Spansion MirrorBit SPI FL Serial Flash

Communication

- USB-UART bridge
- I²C port

Configuration

- SPI and BPI
- Xilinx JTAG interface
- PSoC[®] configuration

Target Applications:

- General FPGA prototyping
- MicroBlaze™ systems
- Configuration development
- USB powered controller
- Cypress[®] PSoC evaluation



